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MS-7317

Version:1.1

CPU:

Intel LGA775 Conroe Processor 1066MHz

System Chipset:

SIS: 968

SIS: 671FX

On Board Chipset:

BIOS -- FWH EEPROM

Azalia Codec -- ALC883/ALC888

LPC Super I/O -- W83627DHG

LAN --RTL8211B/RTL8201CL

CLOCK --RTM876-660

Main Memory:

DDRII * 2

Expansion Slots:

PCI EXPRESS X16 SLOT * 1

PCI EXPRESS X1 SLOT * 1

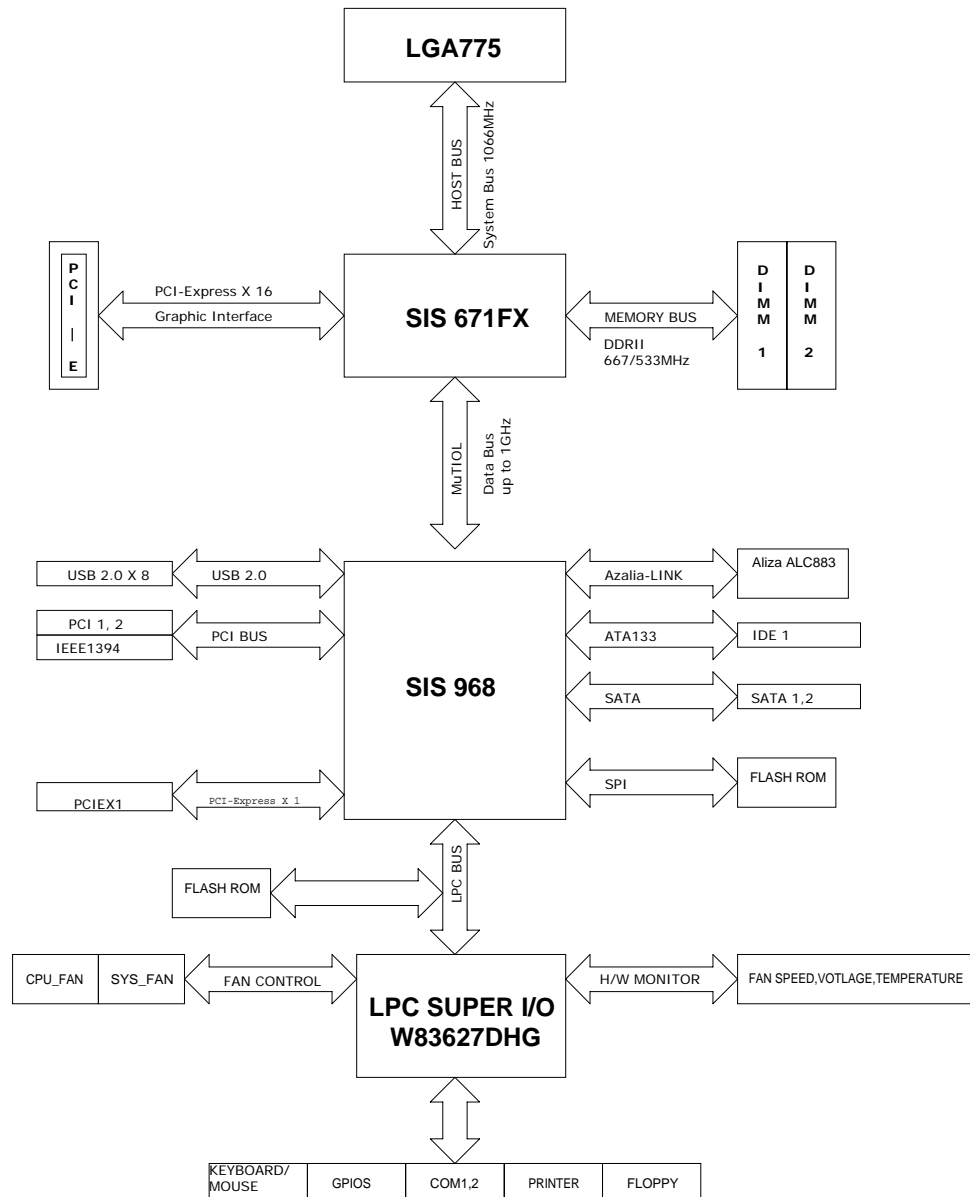
PCI SLOT * 2

PWM:

Controller: ST L6703

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System Block Diagram



GPIO Table on SIS968

GPIO_0	I/O	MAIN	Flash Rom protection
GPIO_1	I/O	MAIN	Pull-Up
GPIO_2	I/O	MAIN	THERM#
GPIO_3	I/O	MAIN	Pull-Up
GPIO_4	I/O	MAIN	Pull-Up
GPIO_5	I/O	MAIN	Pull-Up
GPIO_6	I/O	MAIN	Pull-Up
GPIO_7	I/O	MAIN	Pull-Up
GPIO_8	I/O	RESUME	Pull-Up
GPIO_9	I/O	RESUME	Pull-Down
GPIO_10	I/O	RESUME	Pull-Down
GPIO_11	I/O	RESUME	Pull-Up
GPIO_12	I/O	RESUME	Pull-Up
GPIO_13	I/O	RESUME	Pull-Up
GPIO_14	I/O	RESUME	Pull-Up
GPIO_15	I/O	RESUME	RESERVED
GPIO_16	O	RESUME	RESERVED
GPIO_17	O	RESUME	RESERVED
GPIO_18	O	RESUME	RESERVED
GPIO_19	I/O	MAIN	SMBCLK
GPIO_20	I/O	MAIN	SMBDAT
GPIO_21	I	RESUME	RESERVED
GPIO_22	I	RESUME	RESERVED
GPIO_23	I	RESUME	RESERVED
GPIO_24	I	RESUME	RESERVED

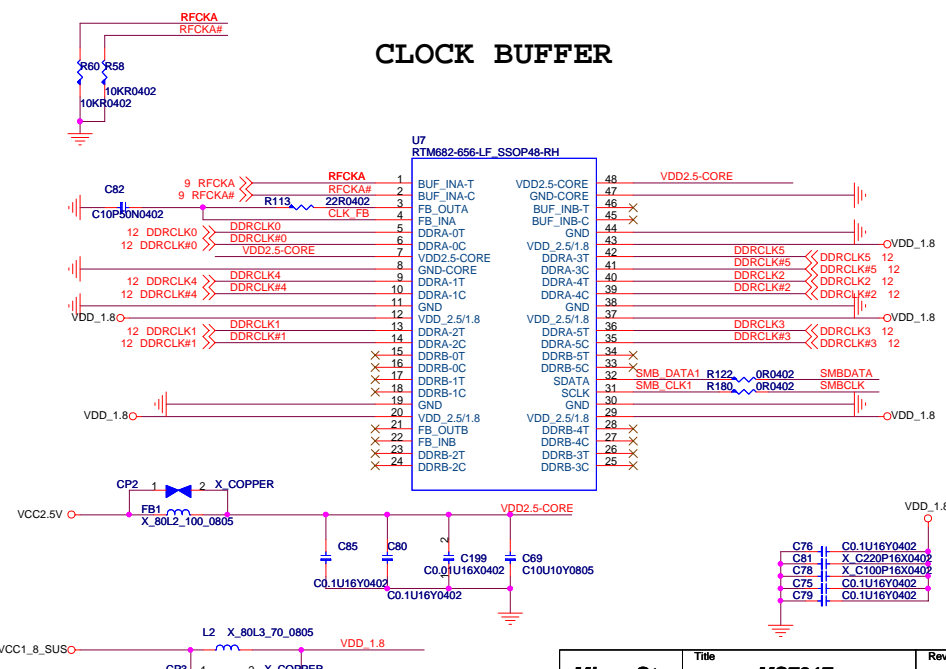
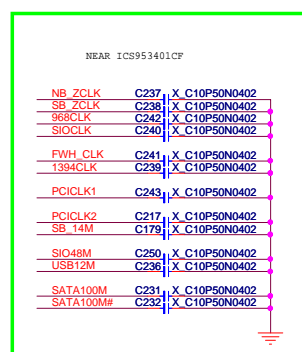
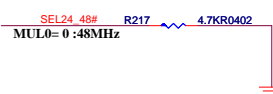
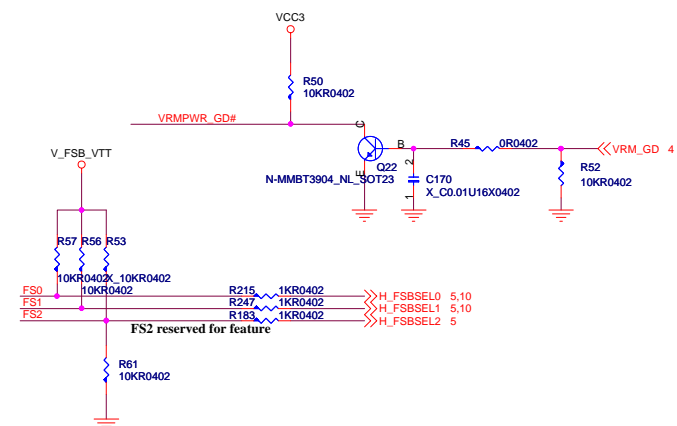
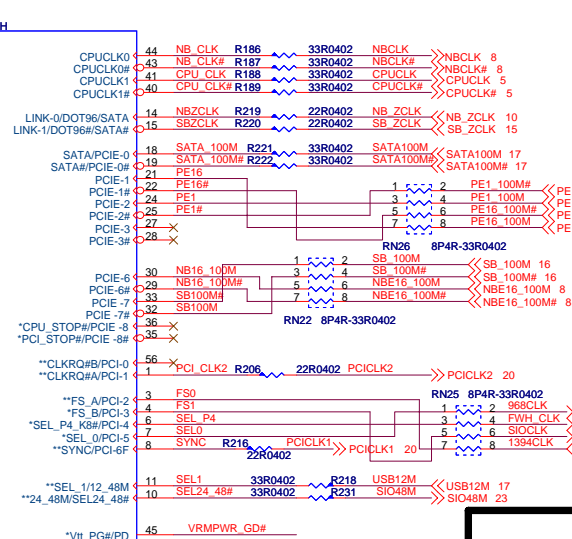
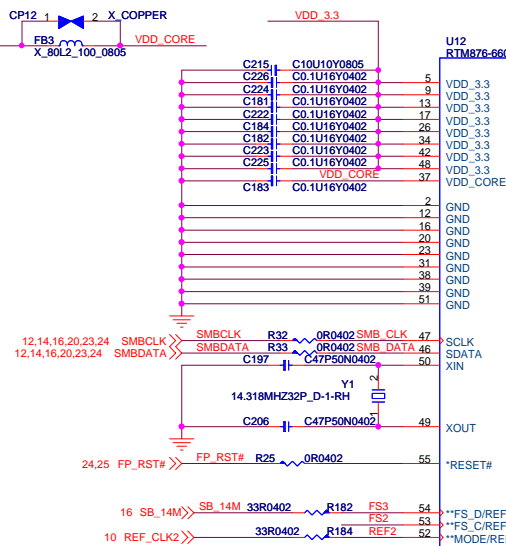
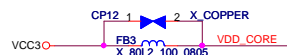
PCI Config.

DEVICE	MCP1 INT Pin	REQ#/GNT#	IDSEL	CLOCK
PCI Slot 1	PIRQ#A PIRQ#B PIRQ#C PIRQ#D	PCI_REQ#0 PCI_GNT#0	AD23	PCICLK1
PCI Slot 2	PIRQ#B PIRQ#C PIRQ#D PIRQ#A	PCI_REQ#1 PCI_GNT#1	AD24	PCICLK2
1394	PIRQ#C	PCI_REQ#3 PCI_GNT#3	AD22	1394CLK

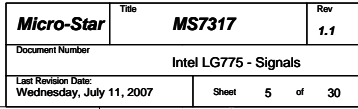
PCI RESET DEVICE

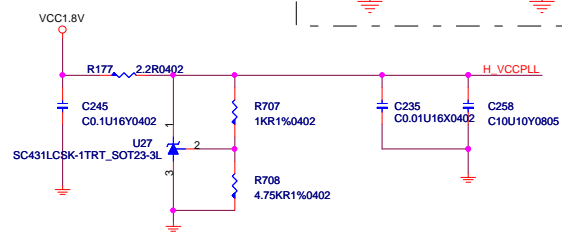
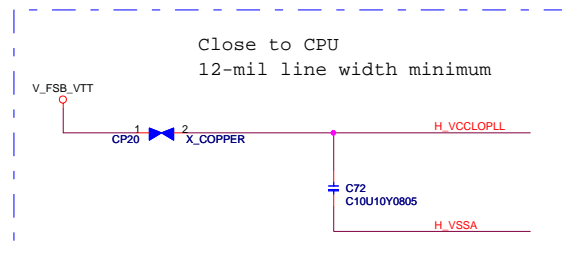
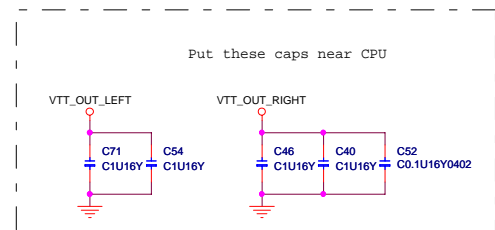
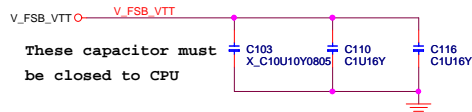
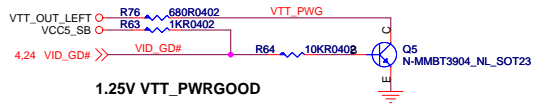
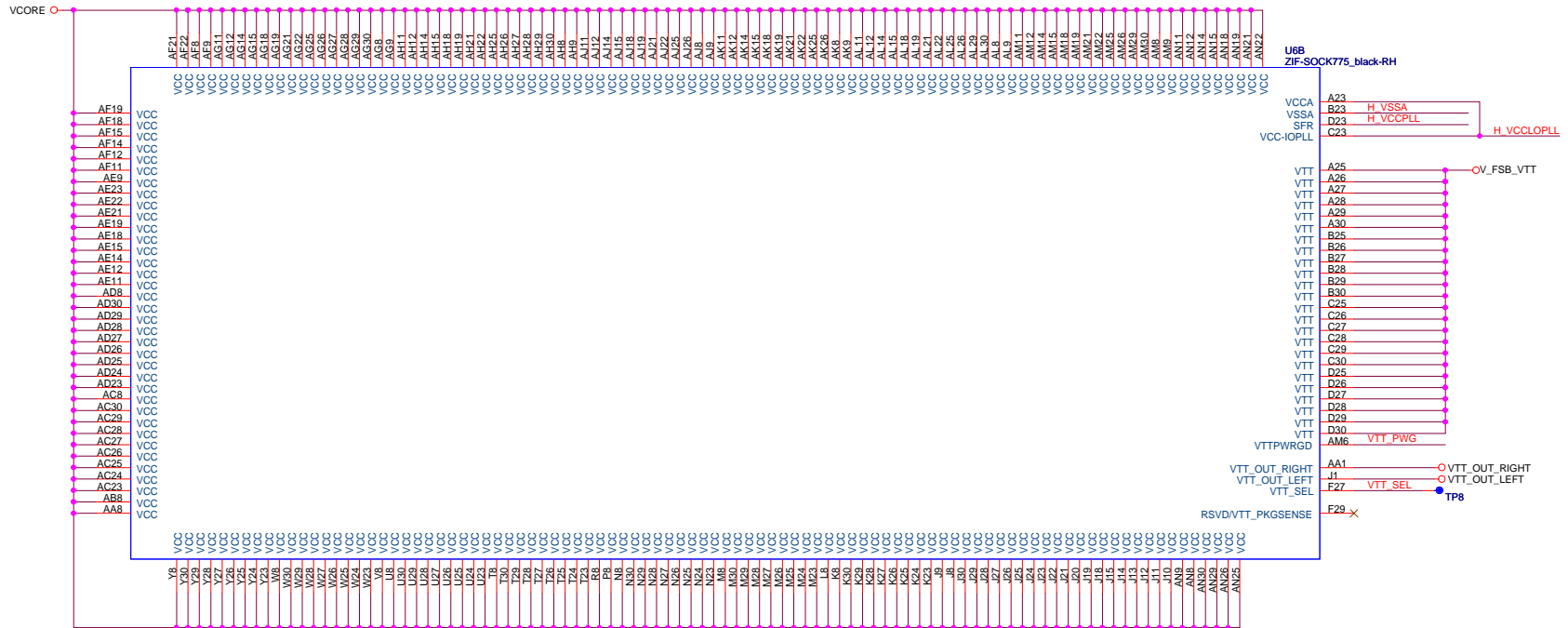
Signals	Target
PCI_RST#	PCIE_X16
SLOT_RST#	PCI1~2, PCI_E1
HD_RST#	Primary IDE
DEV_RST#	LAN
CHIP_RST#	S/IO, Flash Rom, SIS649

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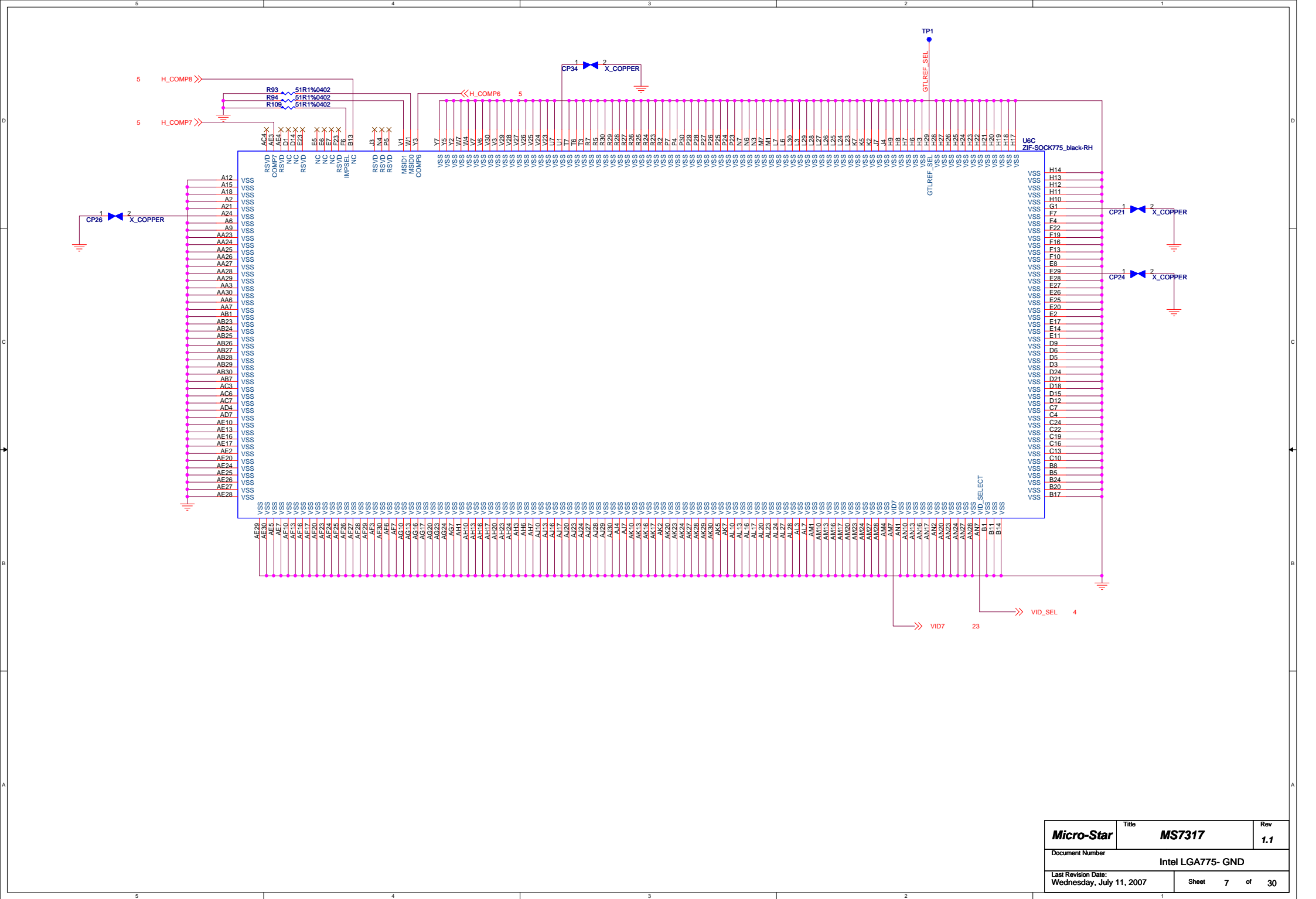


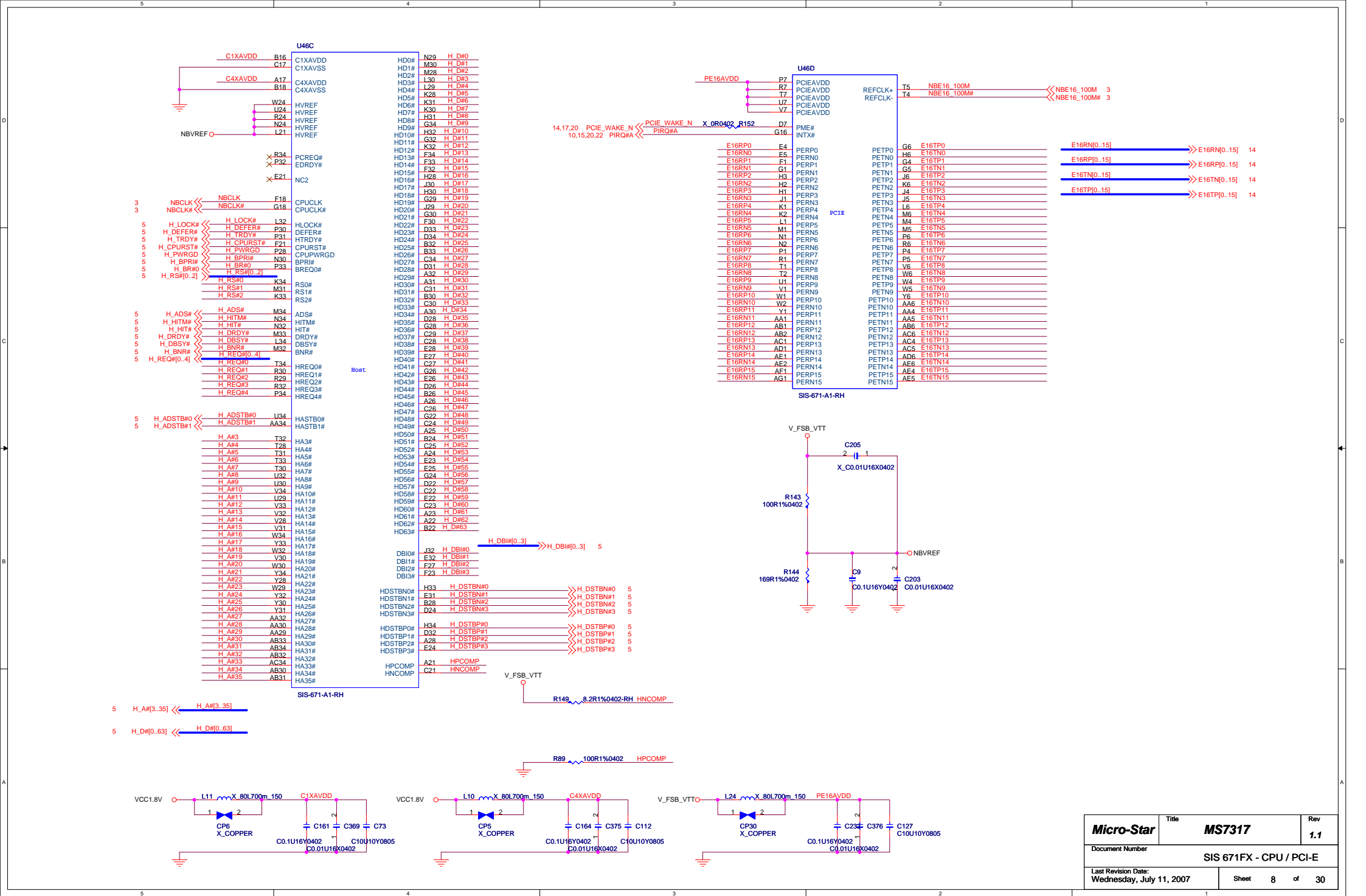
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Clock & Dimm Buffer		
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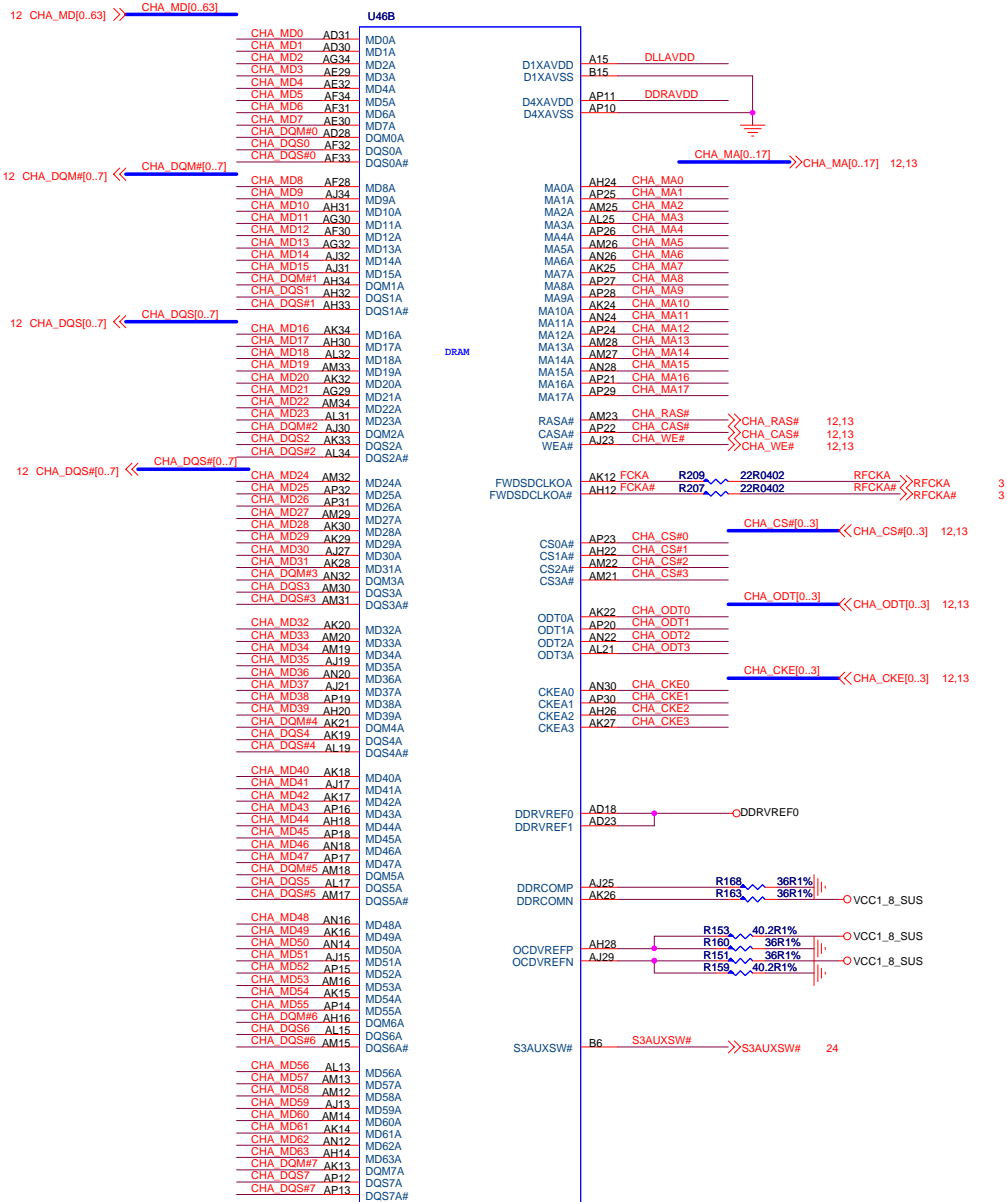




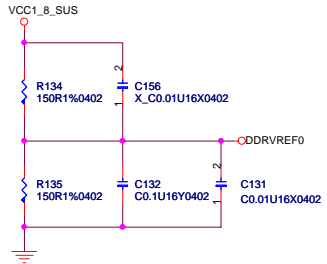
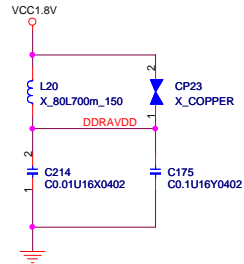
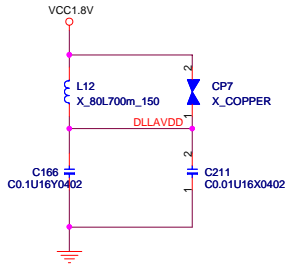
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	Document Number	Intel LG775 - Power		
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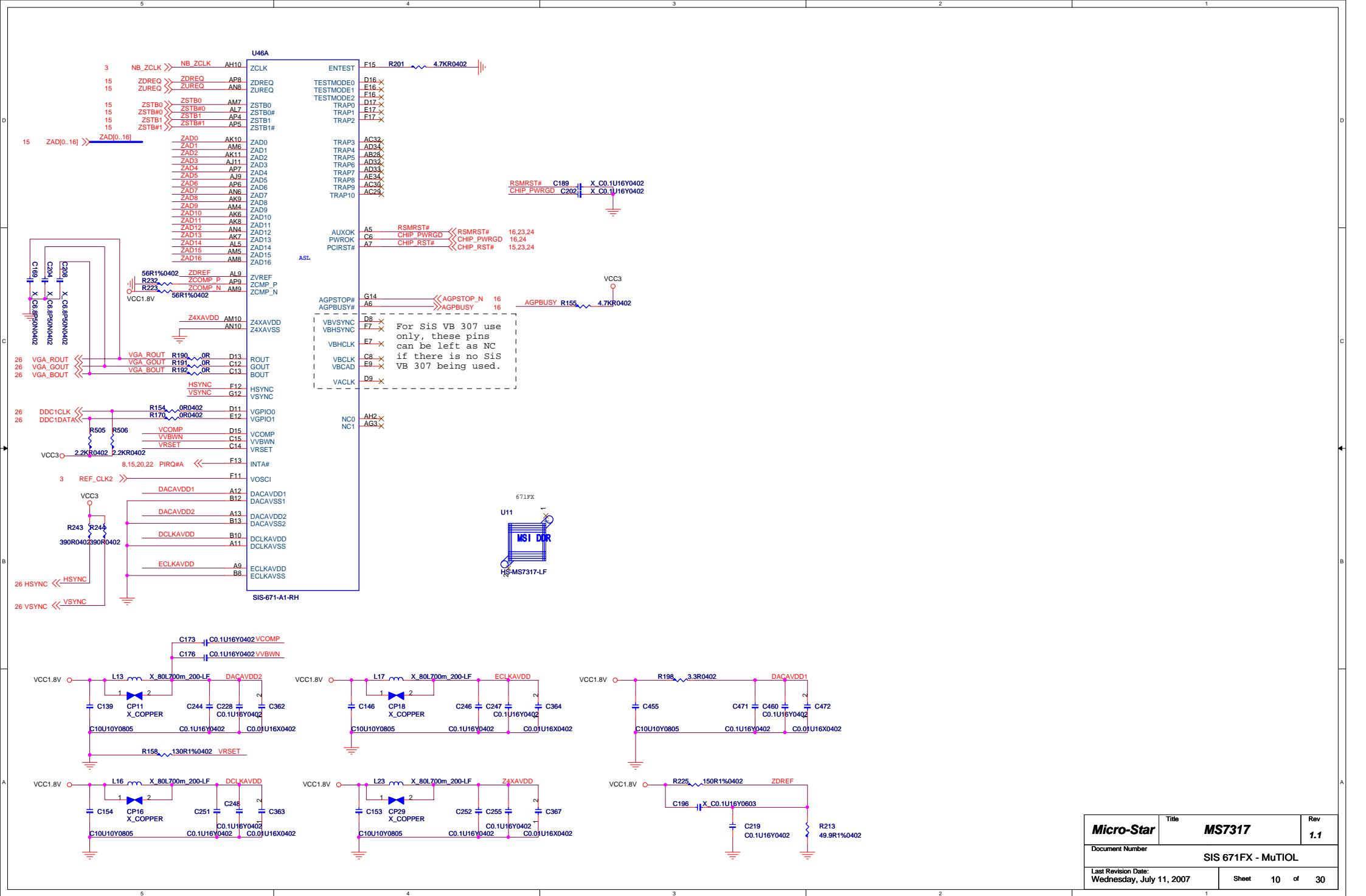




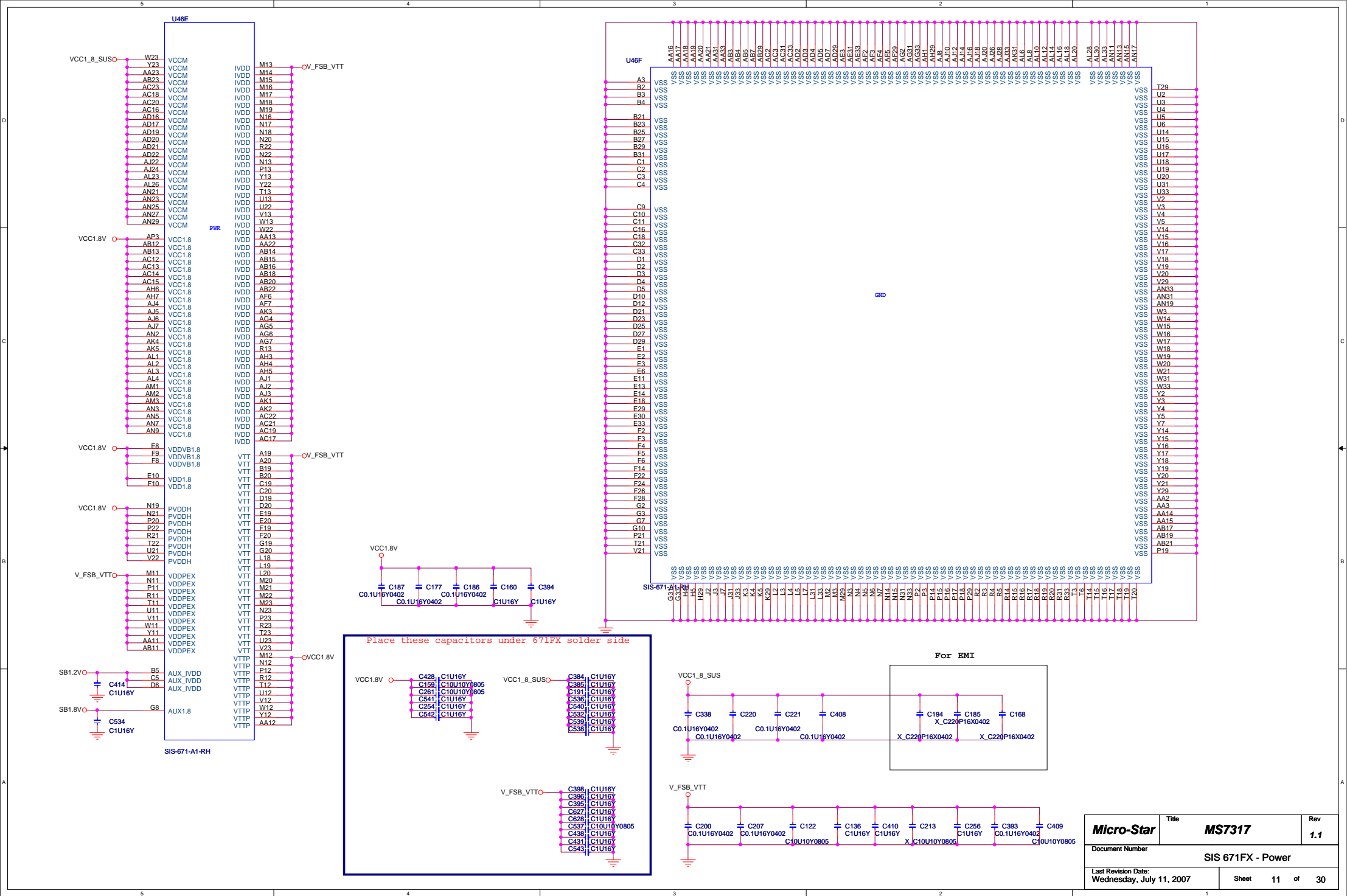
SIS-671-A1-RH

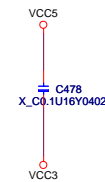
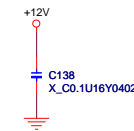
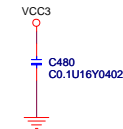
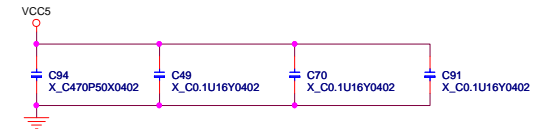
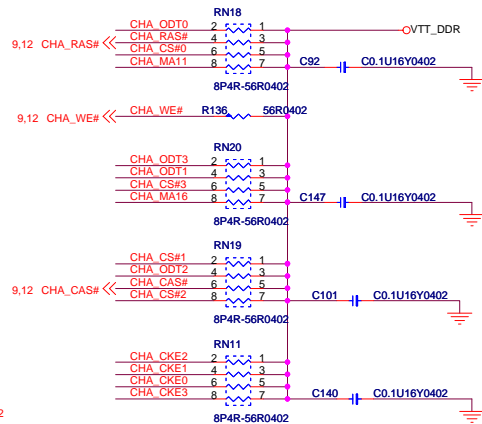
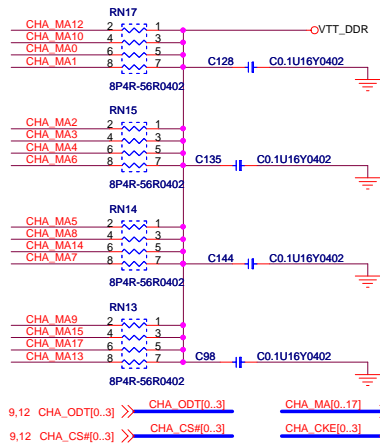


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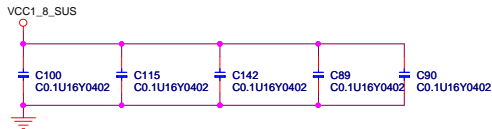


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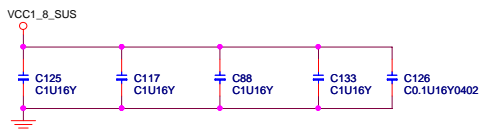




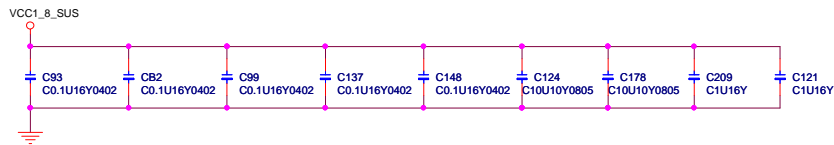
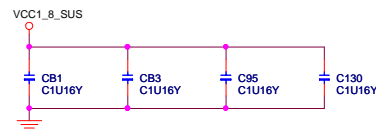
DECOUPLING CAPACITOR FOR SSTL-2 END TERMINATION VTT ISLAND
 0603 Package placed within 200mils of VTT Termination R-packs
 right side of DIMM2



Right side of DIMM1

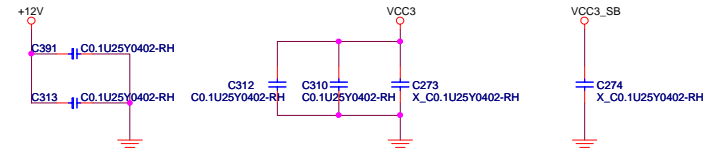


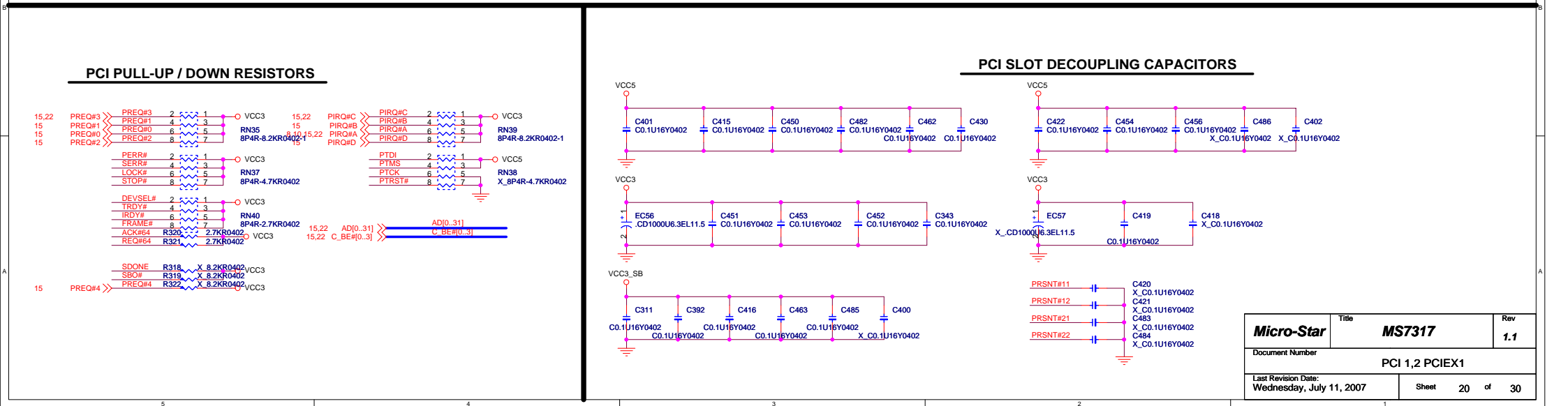
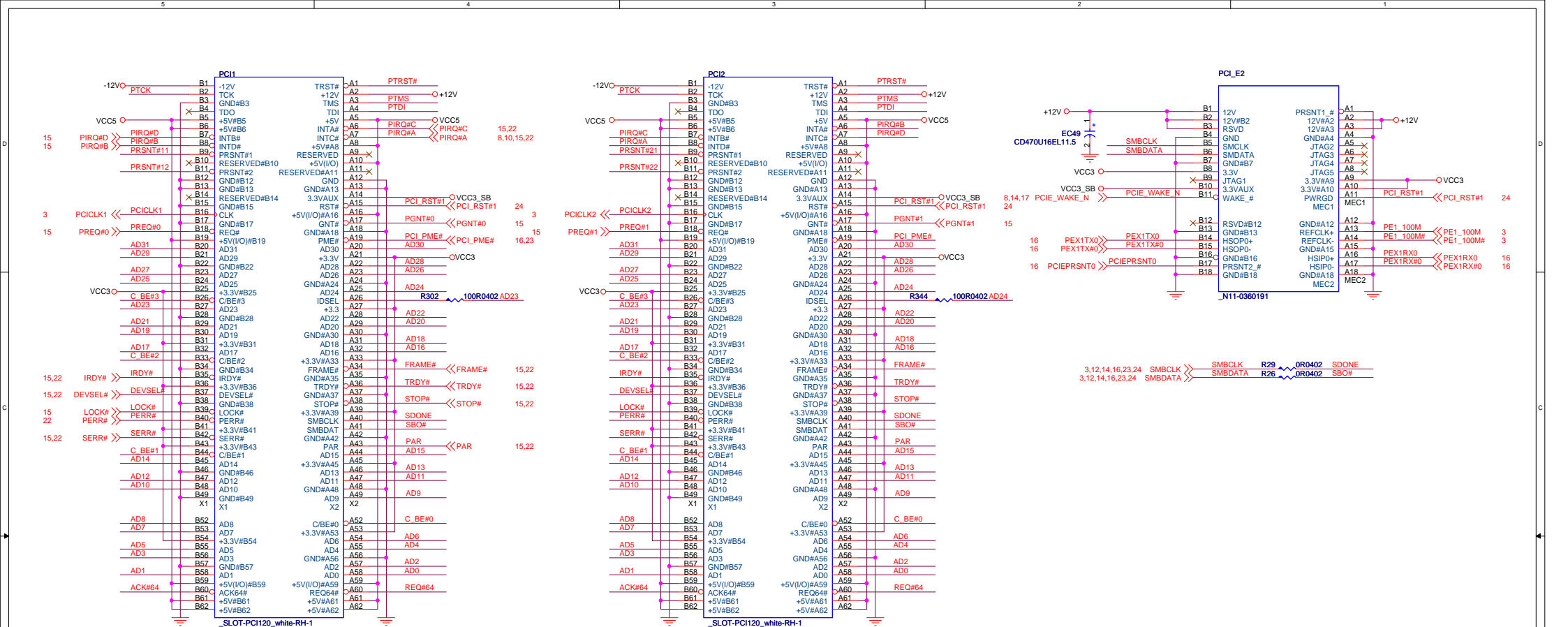
left side of DIMM1

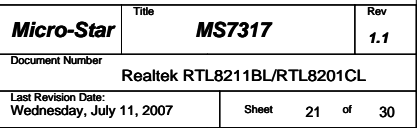


EMI
 Requested

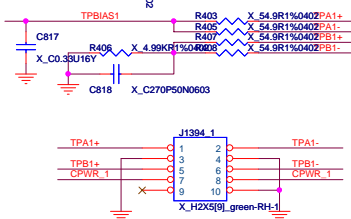
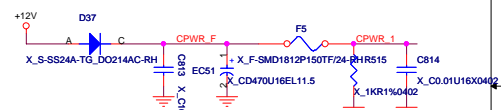
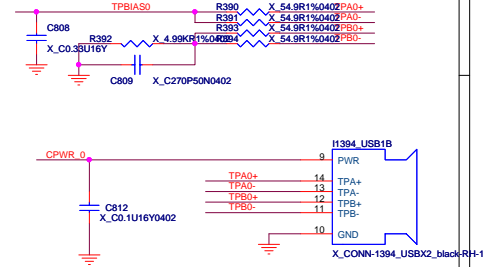
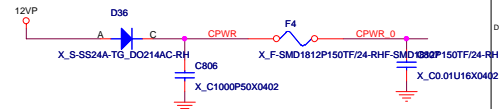
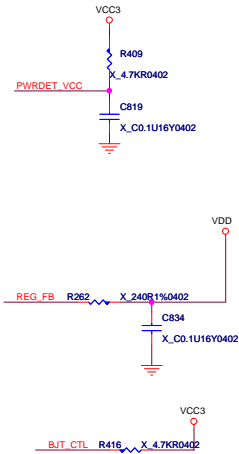
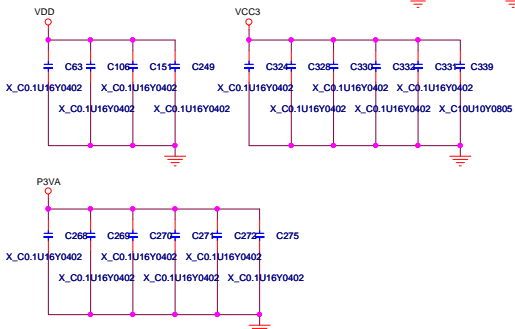
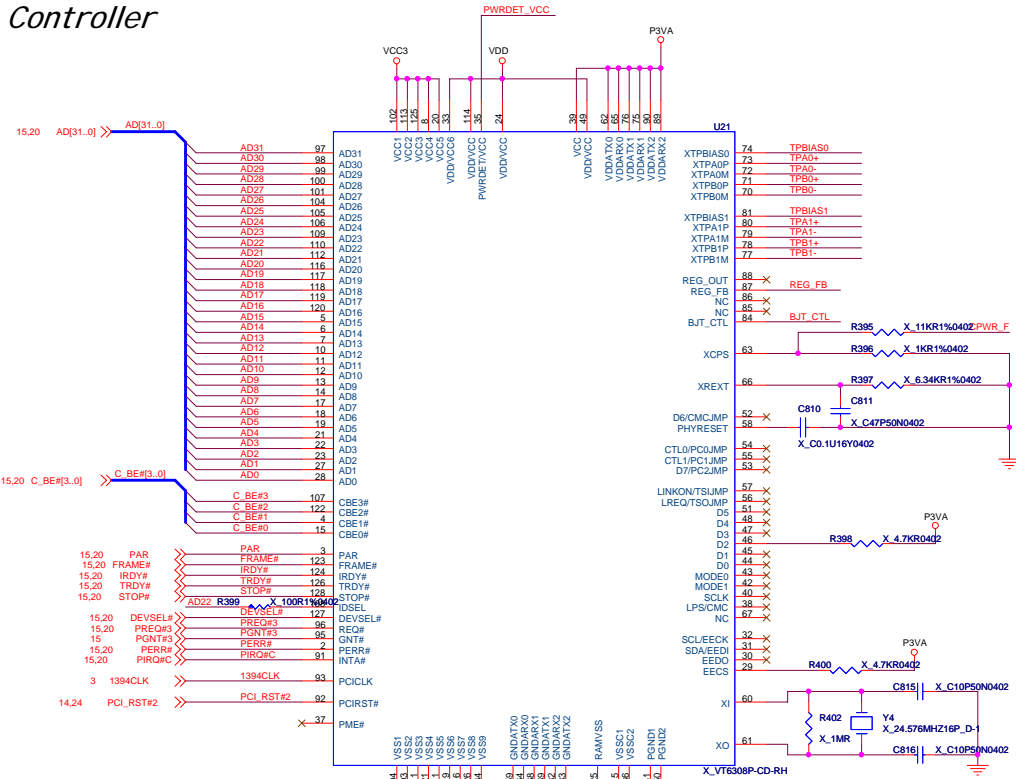
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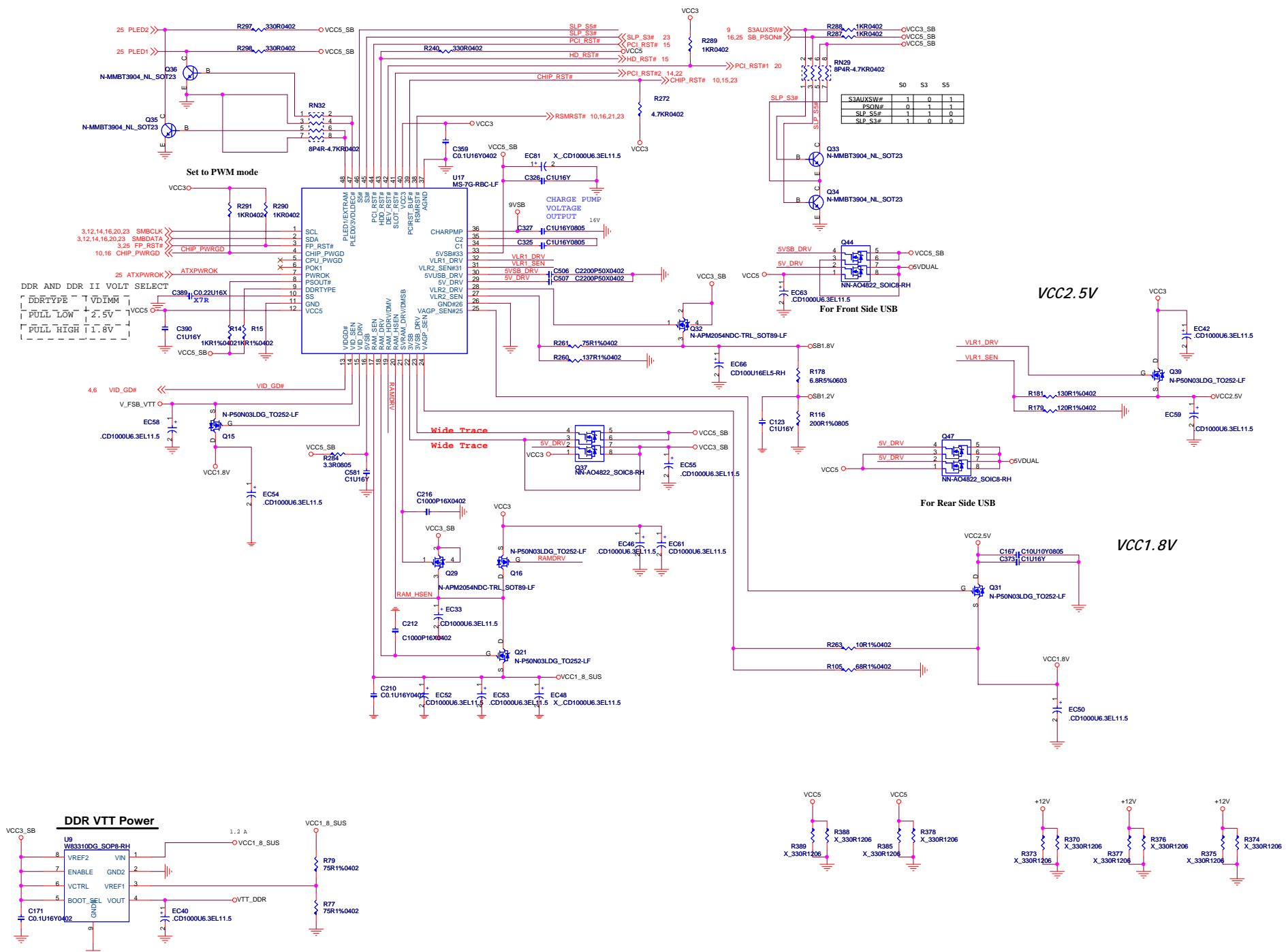




1394a OHCI Link Layer Controller



For Intel 1394 pinheader



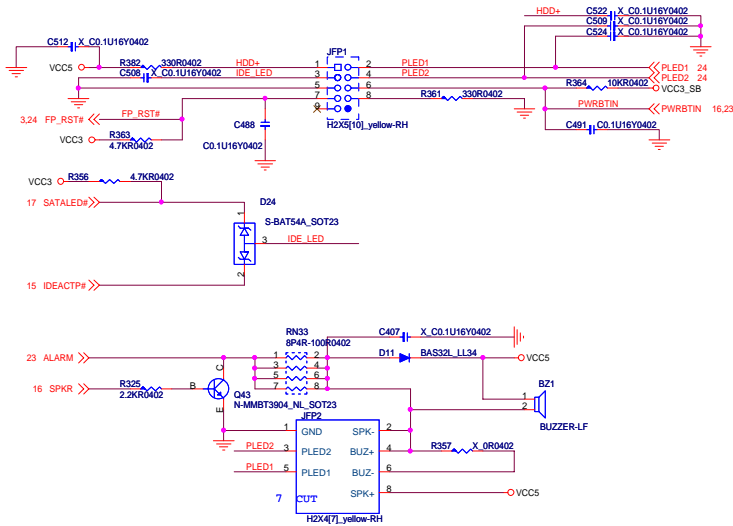
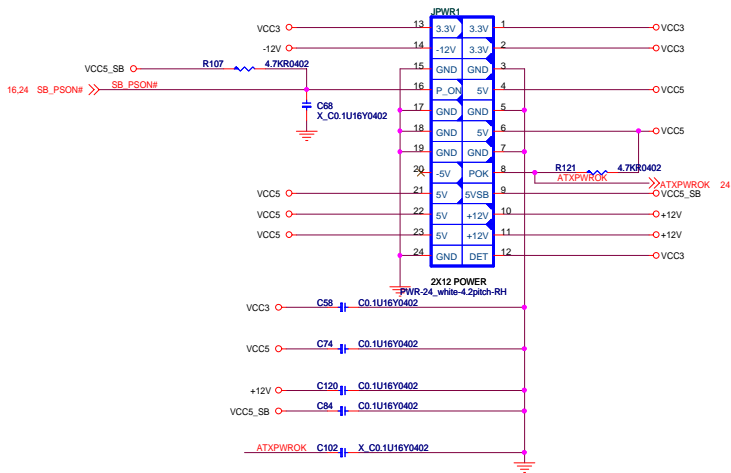
S3AUXSW#	1	0	1
PSON#	0	1	1
SLP_S5#	1	1	0
SLP_S3#	1	0	0

VCC2.5V

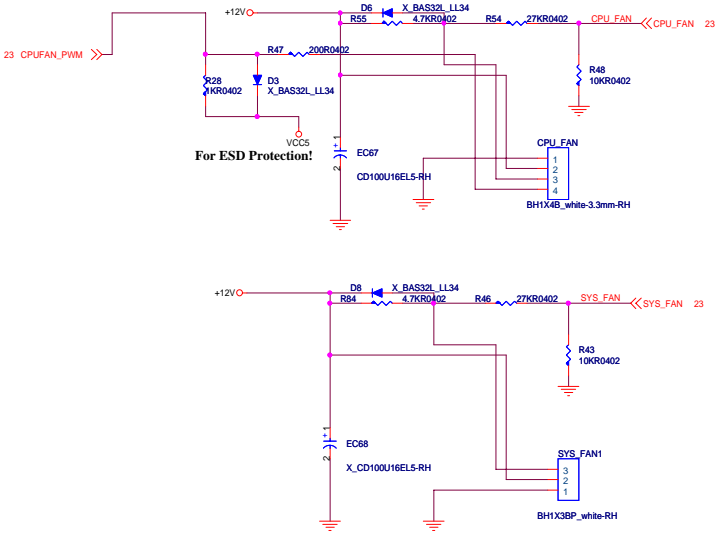
VCC1.8V

DDR VTT Power

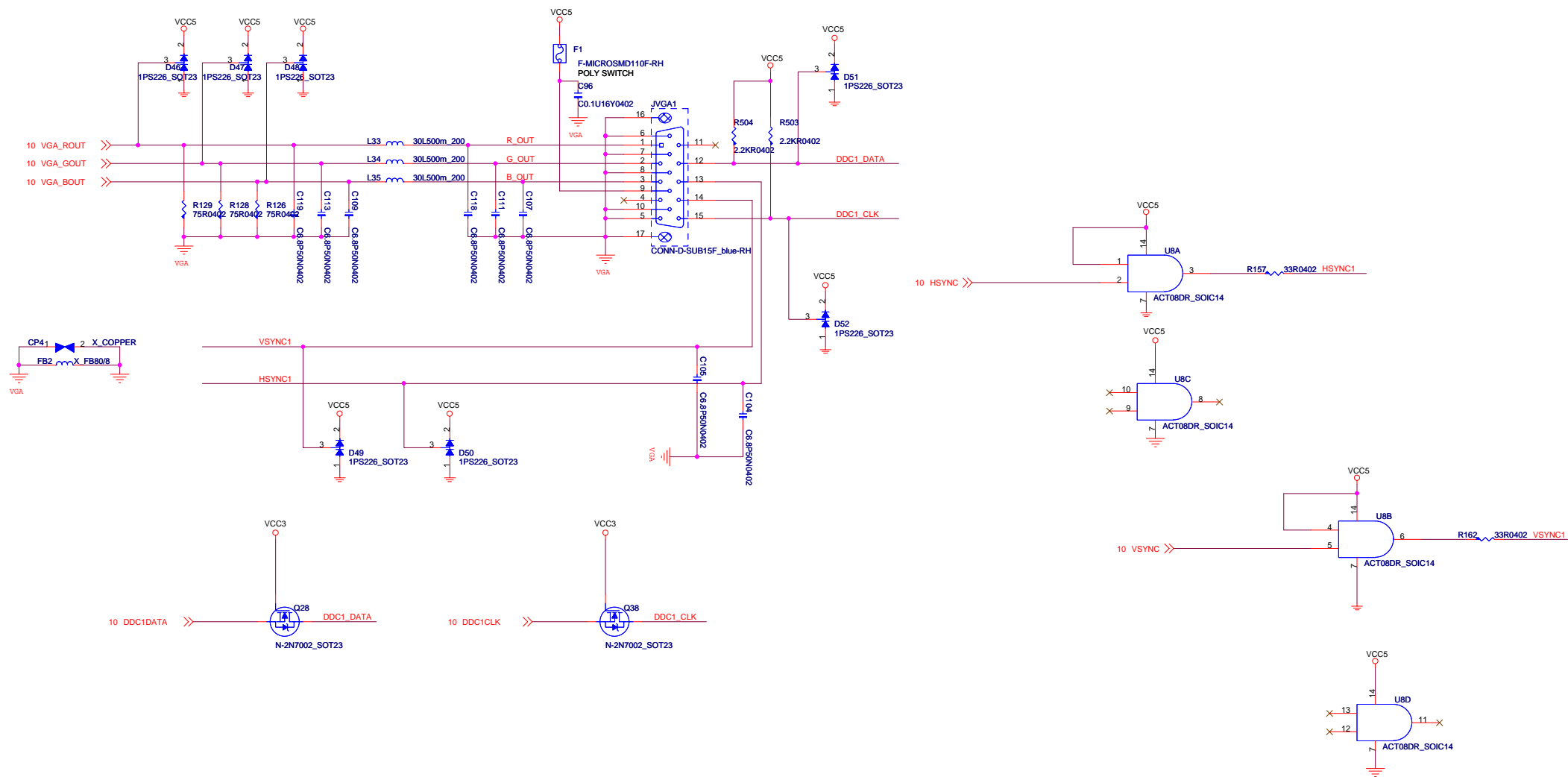
ATX Power and Front Pannel



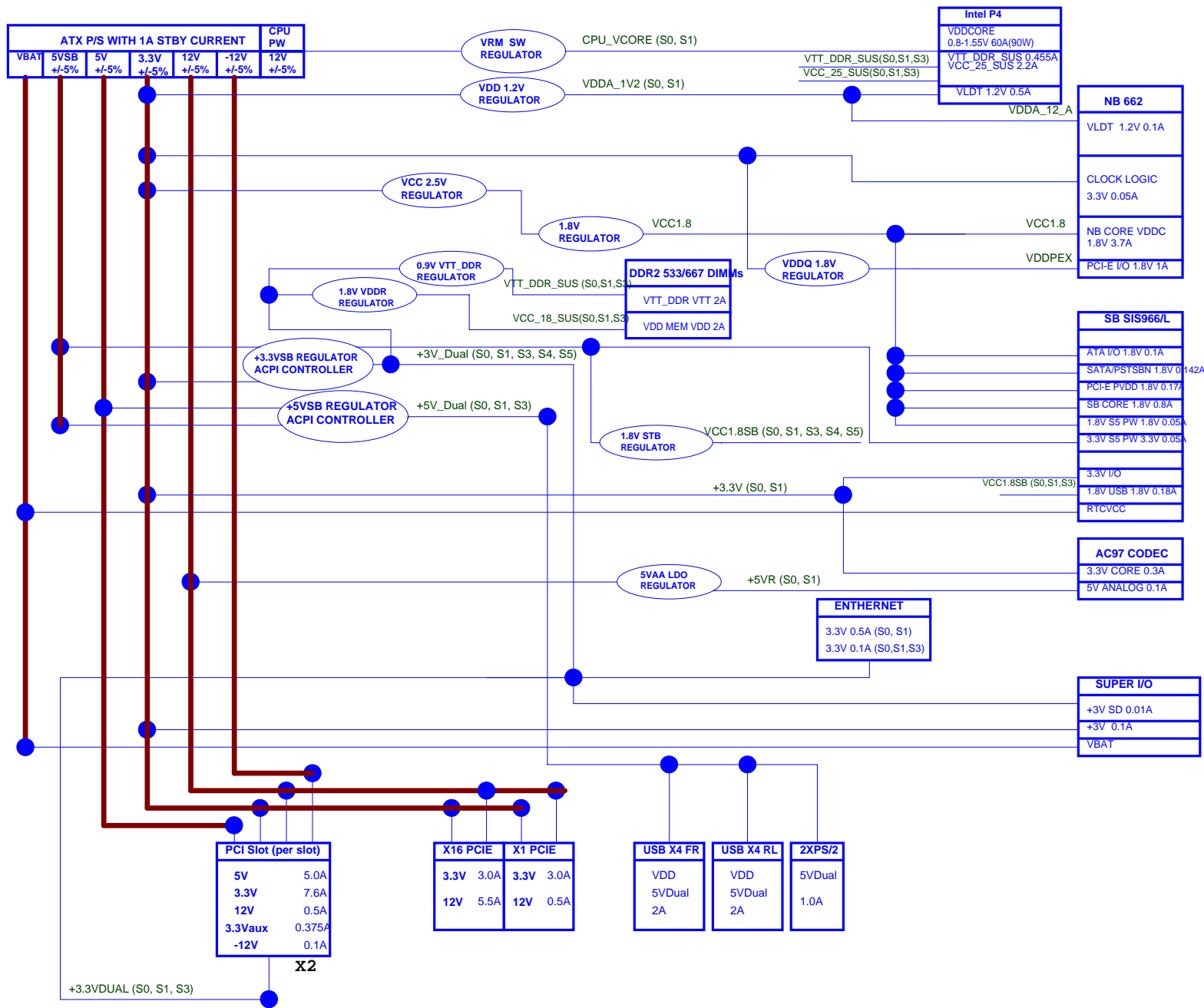
FAN



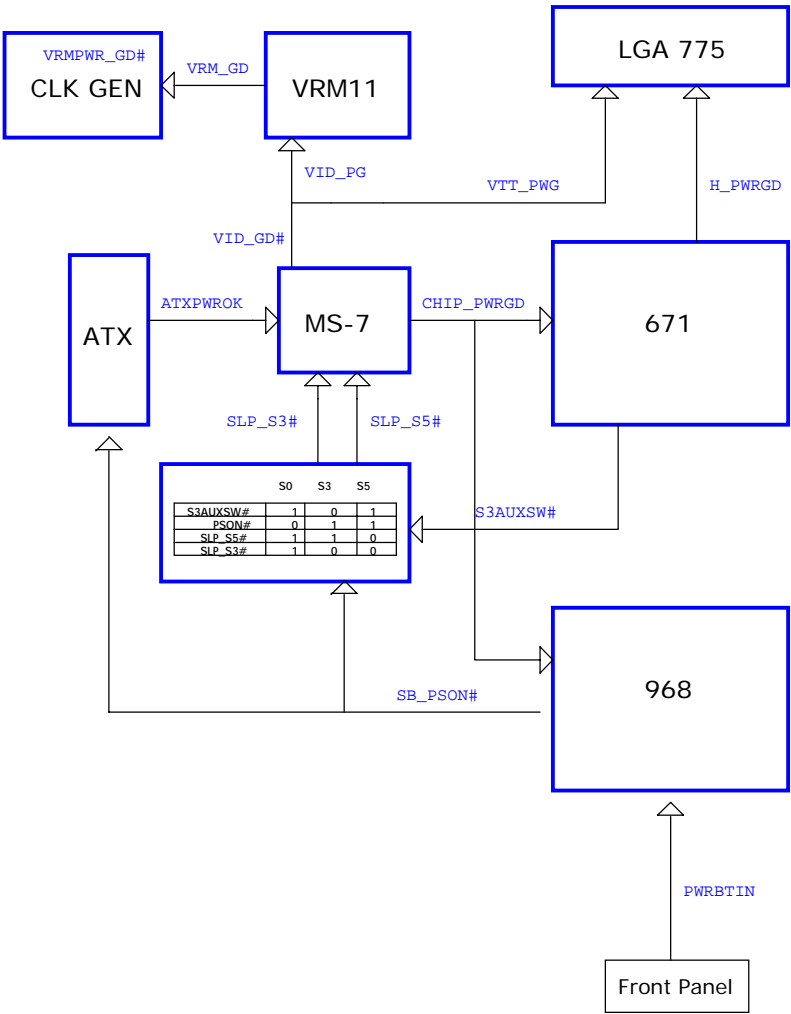
VGA CONNECTOR



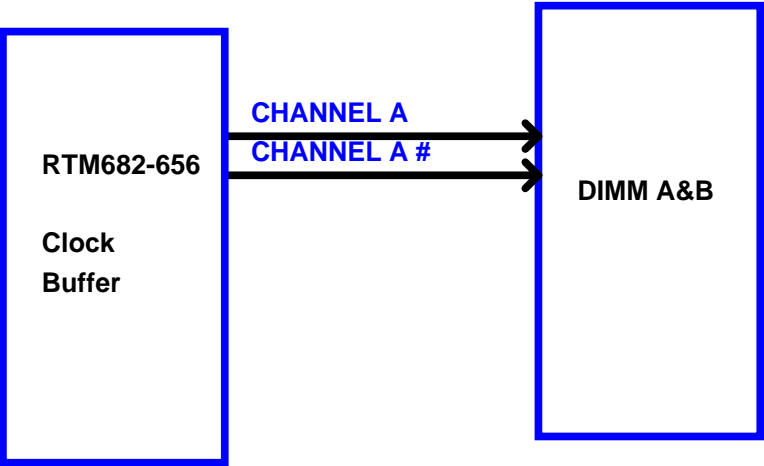
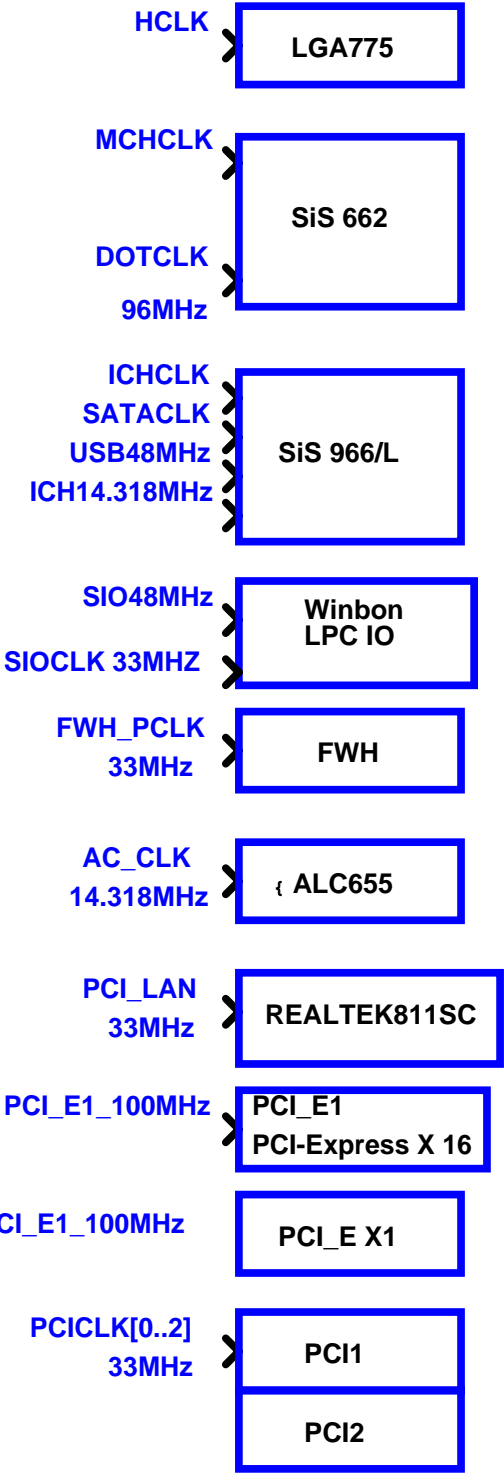
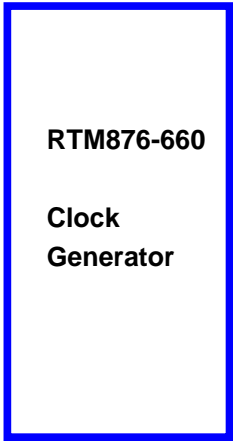
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System Reset Diagram



CLOCK MAP



List Of Change

- 0A----->1.0 2006.05.18
1. EC21 change ASM COMM
 2. Remove R176 R177 R178 R179 RN27 RN21 R236 R237
 3. Remove SIO Sensor for VCC3 -- LPC_+3VIN
 4. R148 chagne to 100 1%ohm
 5. NBVREF change to 0.67.VTT $\overline{2}$ 49.9 to 100ohm
 6. Change the power solution of the 1.8V for NB&SB
 7. Add resistors from 5VCC } 5VDIMM to GND
 8. Add D21 for MS11 application
 9. Remove Debug Port : JLPC1
 10. Follow PM's request ,change Connectors NEW PN
 11. Remove PCREQ#

0312

12. add EC61
13. SWAP HNCOMP & HPCOMP
14. Crystal are both 15pF
15. Hvref from 0.67VTT to 0.63VTT

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